



Zfcl

Docket No.: S0193.0010

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:
Astrid Elbe et al.

Application No.: 10/723,448

Confirmation No.: 5966

Filed: November 25, 2003

Art Unit: 2183

For: PROCESSOR WITH INTERNAL MEMORY
CONFIGURATION

Examiner: D. H. Pan

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed May 30, 2007 (Paper No. 20070522), in which the Examiner has required restriction between the following Groups:

Group I (claims 1-3 and 5-8) are directed to a processor including register memory for storing operands and assign memory space to operands; and

Group II (claims 9 and 11-14) are directed to a computer system with a peripheral device includes a memory configuration unit being designed to make space from the internal memory available for the peripheral device as needed and make space from the internal memory not being made available to the peripheral device available for other data by access via external bus.

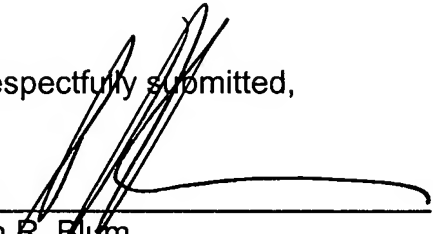
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Applicants hereby elect Group I (claims 1-3 and 5-8) without traverse for examination in this case.

Dated: June 21, 2007

Respectfully submitted,

By 
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